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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/567,759	02/10/2006	Kazumi Naito	Q77022	5647	
23373 SUGHRUE MI	7590 10/17/200 ON, PLLC	8	EXAMINER		
2100 PENNSYLVANIA AVENUE, N.W.			HA, NGUYEN T		
SUITE 800 WASHINGTO	N, DC 20037		ART UNIT	PAPER NUMBER	
			2831		
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			10/17/2008	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)	
	10/567,759	NAITO ET AL.	
Office Action Summary	Examiner	Art Unit	
	NGUYEN T. HA	2831	
The MAILING DATE of this communicate Period for Reply	ion appears on the cover sheet w	ith the correspondence address	
A SHORTENED STATUTORY PERIOD FOR WHICHEVER IS LONGER, FROM THE MAIL - Extensions of time may be available under the provisions of 37 after SIX (6) MONTHS from the mailing date of this communica - If NO period for reply is specified above, the maximum statutor - Failure to reply within the set or extended period for reply will, be any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	ING DATE OF THIS COMMUN CFR 1.136(a). In no event, however, may a ation. y period will apply and will expire SIX (6) MO by statute, cause the application to become A	CATION. reply be timely filed NTHS from the mailing date of this communication BANDONED (35 U.S.C. § 133).	
Status			
1) ☐ Responsive to communication(s) filed on 2a) ☐ This action is FINAL . 2b) ☐ 3) ☐ Since this application is in condition for a closed in accordance with the practice of	☑ This action is non-final. allowance except for formal mat	·	i
Disposition of Claims			
4) Claim(s) 1-21 is/are pending in the applitude 4a) Of the above claim(s) is/are well 5) Claim(s) 19 is/are allowed. 6) Claim(s) 1,3,20 and 21 is/are rejected. 7) Claim(s) 2 and 4-18 is/are objected to. 8) Claim(s) are subject to restriction Application Papers 9) The specification is objected to by the Experimental Experimen	vithdrawn from consideration. and/or election requirement. caminer. accepted or b) □ objected to	•	
Applicant may not request that any objection Replacement drawing sheet(s) including the 11) The oath or declaration is objected to by	correction is required if the drawing	g(s) is objected to. See 37 CFR 1.121(d	i).
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for f a) All b) Some * c) None of: 1. Certified copies of the priority doc 2. Certified copies of the priority doc 3. Copies of the certified copies of the application from the International * See the attached detailed Office action fo	uments have been received. uments have been received in <i>i</i> ne priority documents have beer Bureau (PCT Rule 17.2(a)).	Application No received in this National Stage	
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-93) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	948) Paper No	Summary (PTO-413) s)/Mail Date nformal Patent Application 	

DETAILED ACTION

Response to Arguments

Applicant's arguments with respect to claims 1, 3 and 20-21 have been considered but are most in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 3, and 20-21 are rejected under 35 U.S.C. 102(b) as being anticipated by Kuranuki et al. (US 5,198,967).

Regarding claim 1, Kuranuki et al. disclose a resin molded chip solid electrolytic capacitor comprising a plurality of solid electrolyte capacitor elements (2) horizontally laid in parallel with no gap on a pair of oppositely disposed end parts of a lead frame (3), and a fixing layer (4) which is extending across the plurality of capacitor elements and fixing the capacitor elements with each other (figure 17b).

Regarding claim 3, Kuranuki et al. disclose the fixing layer is an electrically conducting layer (figure 17b).

Regarding claim 20, it is inherent that the Kuranuki et al. capacitor can be used in an electronic circuit.

Regarding claim 21, it is inherent that the Kuranuki et al. capacitor can be used in an electronic device.

Allowable Subject Matter

Claims 2 and 4-18 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Regarding claims 2, and 4-18, the prior art alone or in combination does not teach the limitation of the solid electrolyte capacitor element is produced by stacking an oxide dielectric film layer, a semiconductor layer and an electrically conducting layer in this order to form a cathode part on a surface exclusive of the anode part at one end of an anode substrate comprising a sintered body of a valve-acting metal or an electrically conducting oxide or comprising the sintered body connected with a metal wire.

Claim 19 is allowed.

The following is an examiner's statement of reasons for allowance:

With respect to claim 19, the prior art alone or in combination does not teach the limitation of a method for producing a chip solid electrolyte capacitor comprising the solid electrolyte capacitor element is produced by stacking an oxide dielectric film layer, a semiconductor layer and an electrically conducting layer in this order to form a cathode part on a surface exclusive of the anode part at one end of an anode substrate comprising a sintered body of a valve-acting metal or an electrically conducting oxide or comprising the sintered body connected with a metal wire.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to NGUYEN T. HA whose telephone number is (571)272-1974. The examiner can normally be reached on Monday-Friday from 8:30AM to 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Diego F. Gutierrez can be reached on 571-272-2245. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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